



**Managing Editor Board**

- ❖ Dr. B. D. Venkataamana Reddy  
Dept.of ECE, MITS, A.P, India
- ❖ Dr. Mohsin khan  
Rural Education and Development Foundation, Pakistan
- ❖ Dr. S.Sasikumar  
ANNA university, India
- ❖ Dr. P. Karthigaikumar  
Karunya University, India
- ❖ Dr. Rania Ahmed  
jazan university, Egypt
- ❖ Dr. Roukhe Hassane  
Ismac, Malaysia
- ❖ Dr. Aleksandr Cariow  
West Pomeranian University of Technology, Szczecin, Poland
- ❖ Dr. Swapnadip De  
Jadavpur University, India

**Contact Us**

Website URL : [www.iosrjournals.org](http://www.iosrjournals.org)  
Email : [Support@iosrmail.org](mailto:Support@iosrmail.org)



**Qatar Office:**

IOSR Journals  
Salwa Road  
Near to KFC and Aziz  
Petrol Station,  
DOHA, Qatar

**India Office:**

EHTP, National Highway  
8, Block A, Sector 34,  
Gurugram, Haryana  
122001

**Australia Office:**

43, Ring Road,  
Richmond Vic 3121  
Australia

**New York Office:**

8th floor, Straight hub,  
NS Road, New York,  
NY 10003-9595

IOSR-JVSP

**Contents :**

An Area Efficient FFT Structures Design by Sharing Arithmetic Units	01-07
Design and Synthesis of Multiplexer based Universal Shift Register using Reversible Logic	08-13
P-Wave Related Disease Detection Using DWT	14-18
Design of Majority Logic Decoder for Error Detection and Correction in Memories	19-26
Design And Implementation Of Arithmetic Logic Unit Using Modified Quasi Static Energy Recovery Adiabatic Logic	27-34
Simultaneous Data Path and Clock Path Engineering Change Order for Efficient Timing Closure in Complex SOC	35-41
Design and FPGA Implementation of AMBA APB Bridge with Clock Skew Minimization Technique	42-45
Color Particle Filter Tracking using Frame Segmentation based on JND Color and Spatial Connectivity 4-D Histogram	46-54
Robust Fault Tolerance in Content Addressable Memory Interface	55-63
Analyzing the Impact of Sleep Transistor on SRAM	64-67